TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS IV)

TPC8111

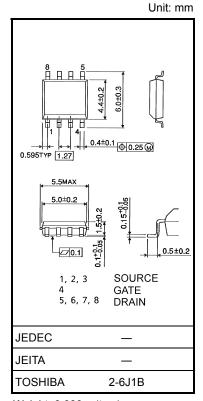
Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

- · Small footprint due to small and thin package
- Low drain-source ON resistance: RDS (ON) = 8.1 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 23 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

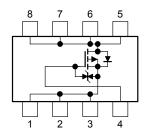
Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	-30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	-30	٧	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	I _D	-11	Α	
	Pulse (Note 1)	I_{DP}	-44	^	
Drain power dissipation (t = 10 s) (Note 2a)		P_{D}	1.9	W	
Drain power dissipation (t = 10 s) (Note 2b)		P _D	1.0	W	
Single pulse avalanche energy (Note 3)		E _{AS}	31.5	mJ	
Avalanche current		I _{AR}	-11	Α	
Repetitive avalanche energy (Note 2a) (Note 4)		E _{AR}	0.19	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note 1, Note 2, Note 3 and Note 4: See the next page. This transistor is an electrostatic-sensitive device. Please handle with caution.



Weight: 0.080 g (typ.)

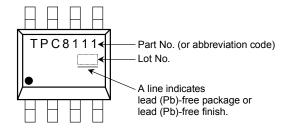
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R _{th (ch-a)}	125	°C/W

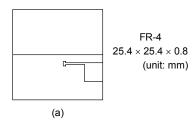
Marking (Note 5)

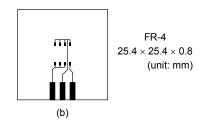


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board (a) (b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = -24~V,~T_{ch} = 25^{\circ}C$ (initial), L = 0.2 mH, R_G = 25 $\Omega,~I_{AR} = -11~A$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

Weekly code: (Three digits)
 Week of manufacture
 (01 for the first week of a year: sequential number up to 52 or 53)
 Year of manufacture
 (The last digit of a year)

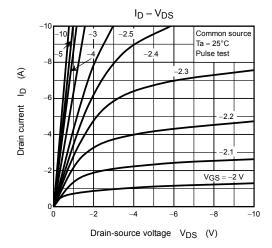
Electrical Characteristics (Ta = 25°C)

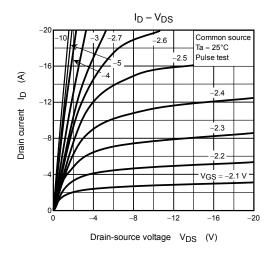
Characte	eristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-OFF current		I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
		V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	v
Gate threshold voltage	e	V _{th}	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source ON resistance		D	$V_{GS} = -4 \text{ V}, I_D = -5.5 \text{ A}$	_	12	18	mΩ
		R _{DS} (ON)	$V_{GS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	_	8.1	12	
Forward transfer admittance		Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	11	23	_	S
Input capacitance		C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	5710	_	pF
Reverse transfer capacitance		C _{rss}		_	560	_	
Output capacitance		Coss		_	590	_	
Switching time	Rise time	t _r	V _{GS} -10 V V _{GS} V _{GS}	_	18	_	ns ns
	Turn-ON time	t _{on}		_	23	_	
	Fall time	t _f		_	109	_	
	Turn-OFF time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty \leq 1%, $t_W = 10 \mu\text{s}$	_	396	_	
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -24 \text{ V}, V_{GS} = -10 \text{ V},$	_	107	_	nC
Gate-source charge 1		Q _{gs1}	$I_D = -11 \text{ A}$		12		
Gate-drain ("miller") charge		Q _{gd}		_	20	_	

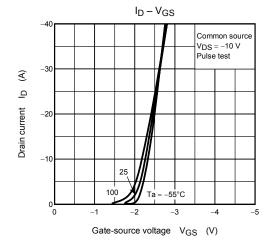
Source-Drain Ratings and Characteristics (Ta = 25°C)

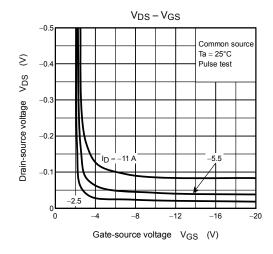
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP}	_	_	_	-44	Α
Forward voltage (diode)	V _{DSF}	$I_{DR} = -11 \text{ A}, V_{GS} = 0 \text{ V}$		_	1.2	V

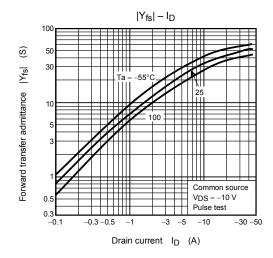
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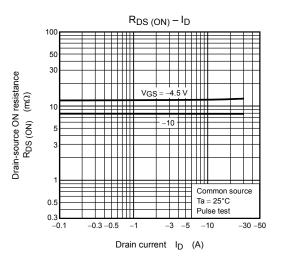


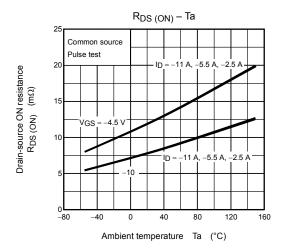


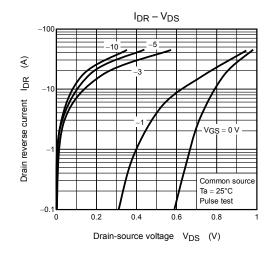


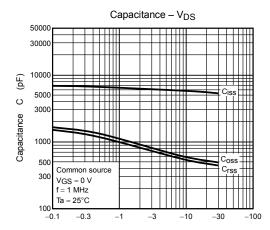


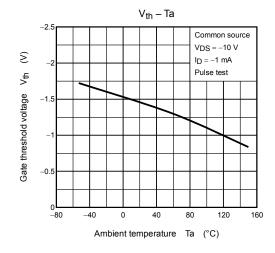




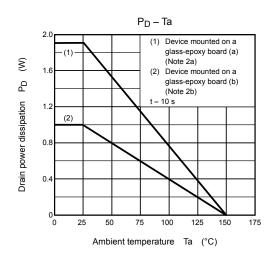


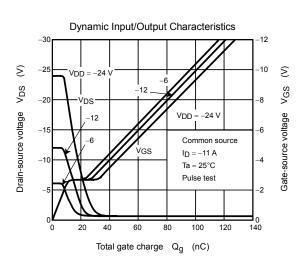




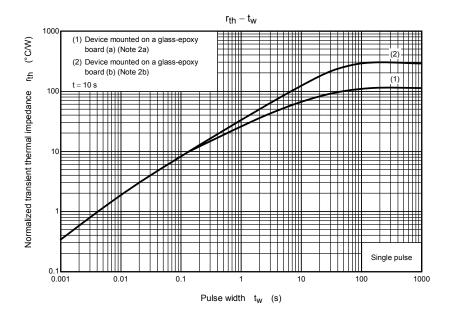


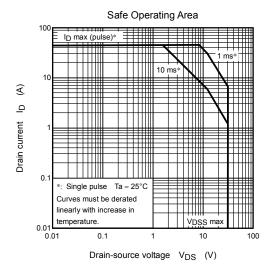
Drain-source voltage V_{DS} (V)





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